

Notice of References Cited	Application/Control No. 10/525,173	Applicant(s)/Patent Under Reexamination YOUNG, NIGEL D.	
	Examiner Julio J. Maldonado	Art Unit 2823	Page 1 of 1

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-7,176,068 B2	02-2007	Kitakado et al.	438/149
*	B	US-6,027,960	02-2000	Kusumoto et al.	438/166
*	C	US-5,480,810	01-1996	Wei et al.	438/59
*	D	US-5,079,426	01-1992	Antonuk et al.	250/370.09
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	L	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, Volume 1: Process Technology, 1986 by Lattice Press, page 179.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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